IN THE SPECIFICATION:

A Substitute Specification and Abstract are attached herewith together with a marked copy of the Specification and Abstract showing the changes made. No new matter is introduced in this Substitute Specification. The Substitute Specification includes the substitute pages (annexes) entered in the PCT prosecution.

IN THE CLAIMS:

On substitute page 7 of the claims, delete "Patent Claims" and substitute --**I CLAIM AS MY INVENTION**--.

Please cancel claims 1-7 on substitute pages 7-9 without prejudice.

Please substitute claims 8-11 as follows:

8. A circuit arrangement for receiving data arranged in a transmission frame, whereby different time slot widths on a same transmission link can be configured within the transmission frame, comprising:

a first processing unit for readout of data out from a current time slot and offering current state parameters of the current time slot, for intermediately storing state parameters of a time slot following the current time slot, and for intermediately storing readout data of the time slot in a first memory unit;

a second processing unit with an allocation unit for administering a second memory unit in which state parameters read from the first memory unit given a time slot change are stored, for editing the state parameters intermediately stored in the first memory unit, and for

allocation of the data of the current time slot intermediately stored in the first memory unit into a third memory unit; and

a third processing unit for forming data words from the data deposited in the third memory unit.

9. A circuit arrangement for transmitting data arranged in a transmission frame, whereby different time slot widths on a same transmission link can be configured within the transmission frame, comprising

a first processing unit that comprises a unit for reading data in a current time slot into the transmission frame and offering current state parameters for the current time slot, and a first memory unit for intermediately storing state parameters of a time slot following the current time slot;

a second processing unit with an allocation unit for administering a second memory unit in which state parameters read from the first memory unit given a time slot change are stored, for editing the state parameters to be intermediately stored in the first memory unit, and for allocation of data intermediately stored in a third memory unit into the first memory unit; and

a third processing unit for allocation of data belonging to time slots and storing them in a third memory unit.

10. A method for reception of data arranged in a transmission frame, different time slot widths on a

same transmission link being configured within the transmission frame, comprising the steps of:

reading out data from a current time slot, intermediately storing the data, and offering current state parameters of the current time slot;

intermediately storing in a first memory unit state parameters of a time slot following the current time slot;

storing and administering in a second memory unit state parameters read from the first memory unit given a time slot change;

reading into a third memory unit the state parameters intermediately stored in the first memory unit which are offered and the data of the current time slot intermediately stored in the first memory unit; and

forming data words from the data deposited in the third memory unit.

11. A method for transmission of data arranged in a transmission frame, different time slot widths on a same transmission link being configured within the transmission frame, comprising the steps of:

offering state parameters of a current time slot and reading data into a current time slot;

intermediately storing state parameters of a time slot following the current time slot in a first memory unit;

offering in a second memory unit the state parameters intermediately stored in the first memory unit in which state parameters read from the first memory unit